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U.S. UTILITY Patent Application

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g Kunal Par			<u> </u>	L	7 1 10	114.87

Kunal Parekh John Zahurak Phillip Wald

Capacitors, DRAM arrays, monolithic integrated circuits, and monolithic integrated circuits, and integrated circuits. The integrated circuits are integrated circuits.

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ISSUING	CLASSIFICATION
ORIGINAL	CROSS REFERENCE(S)
CLASS SUBCLASS CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)
INTERNATIONAL CLASSIFICATION	
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TERMINAL DISCLAIMED	DRAWINGS		CLAIMS ALLOWED			
DISCLAIMER	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.	
The term of this patent				NOTICE OF ALLOWANCE MAILED		
subsequent to (date) has been disclaimed.	(Assistant Examiner)		(Date)			
The term of this patent shall not extend beyond the expiration date					• ·	
of U.S Patent. No.				ISSUE FEE		
	(Primary E	Exeminer)	(Date)	Amount Due	Date Paid	
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